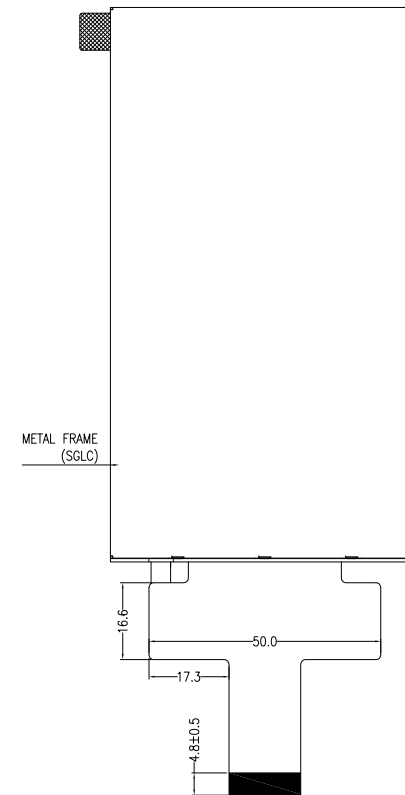
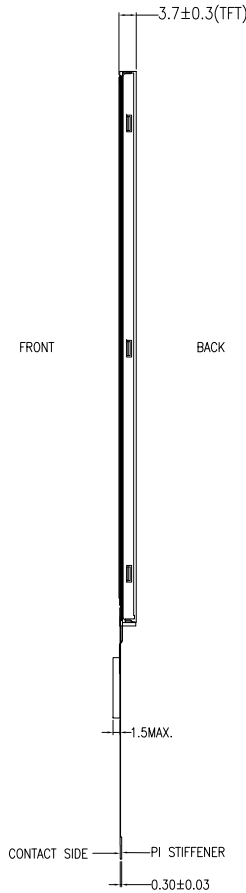
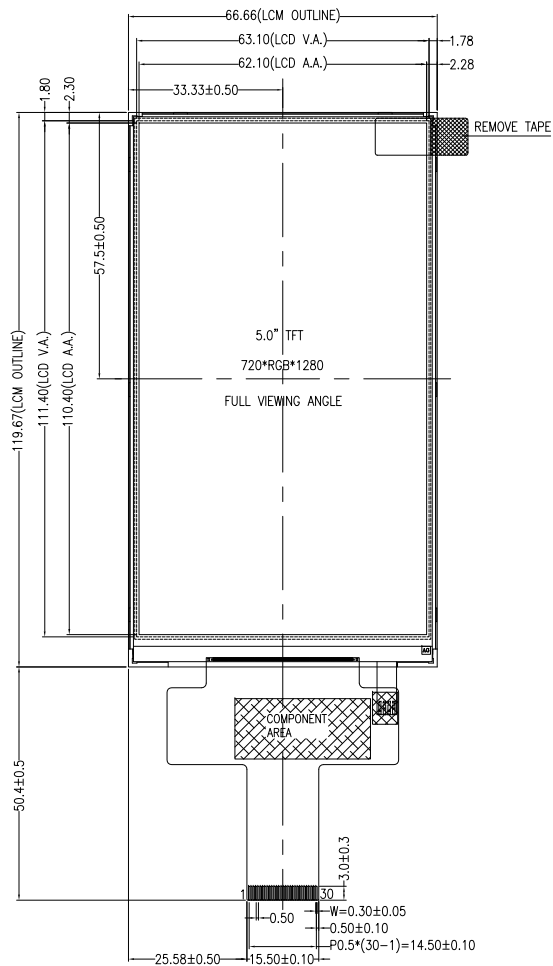
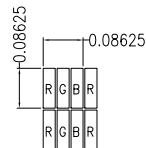




PIN ASSIGNMENT FOR LCM	
PIN	SYMBOL
1	GND
2	GND
3	LRSTB
4	LPTE
5	VCI
6	VCI
7	LCD_ID
8	MIPI_TDN3
9	MIPI_TDP3
10	GND
11	MIPI_TDN2
12	MIPI_TDP2
13	GND
14	LEDA
15	LEDA
16	LEDK
17	LEDK
18	GND
19	MIPI_TCN
20	MIPI_TCP
21	GND
22	MIPI_TDN0
23	MIPI_TDP0
24	GND
25	MIPI_TDN1
26	MIPI_TDP1
27	GND
28	NC
29	NC
30	NC

NOTES FOR TFT:

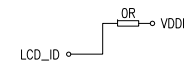
1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY BLACK
2. OPERATING VOLTAGE: VCI=2.8V (TYP.)
3. VIEWING DIRECTION: FULL VIEWING ANGLE
4. DRIVER IC: ILI9881C
5. OPERATING TEMP.: -20°C ~ 70°C
6. STORAGE TEMP.: -30°C ~ 80°C
7. LED BACKLIGHT: WHITE LED, If=60mA, Vf=19.6V ~ 23.8V
8. SURFACE LUMINANCE: 400CD/M² (TYP.)
9. TOP POLARIZER SURFACE TREATMENT: ANTI-GLARE
10. RECOMMEND MATCHING CONNECTOR: HRS FH34SRJ-30S-0.5SH OR EQUIVALENT
11. ONLY THE SINGLE LAYER AREA ON FPC IS BENDABLE AND THE MINIMUM BEND RADIUS IS 1.5MM
12. GENERAL TOLERANCE: ±0.3MM
13. ROHS COMPLIANT



PIXEL DETAIL
(SCALE 100:1)



BACKLIGHT CIRCUIT DIAGRAM



MULTI-INNO TECHNOLOGY CO., LTD.

DRAWN BY:	RITA	2024.03.04		MODULE P/N:
CHECKED BY:			SCALE : 1/1	MIO500APT-1
APPROVED BY:			UNIT: mm	DESCRIPTION:
VERSION NO:	01		SHEET NO.: 1/1	TFT MODULE

01	FIRST ISSUE	RITA	2024.03.04
VER.	REVISED DESCRIPTION	REVISER	DATE